

Form PTO-1449 <div style="text-align: center;"> INFORMATION DISCLOSURE CITATION IN AN APPLICATION </div> <div style="text-align: center; margin-top: 10px;"> (Use several sheets if necessary) </div>			ATTY DOCKET NO. 12480-000017/US		APPLICATION NO. NEW APPLICATION	
			APPLICANT Koichi HANAFUSA, Takao KANZAKI		CONF. NO. Unknown	
			FILING DATE July 22, 2003		GROUP Unknown	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
W	5,859,757	1/12/1999	Hanafusa et al.	361	100	
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
OTHER DOCUMENTS (Include Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.						
W		KAZUO WATANABE, "Designing of Practical Analog Electronic Circuits", Sougou-Denshi Shuppansha, June 22, 1996, pages 109 - 112.				
EXAMINER		DATE CONSIDERED				
NGUYEN, K		9/10/04				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						